

ABSTRACT OF THE DISCLOSURE

A method for calibrating a VCO within a phase locked loop circuit is disclosed. First, a DAC output voltage is set to its minimum, and a counter M is adjusted until a comparator is its threshold voltage. Next, the DAC is set to another voltage, and counter M is again adjusted to the comparator threshold. This process is repeated for as many steps as desired. When the phase locked loop circuit requests an instantaneous frequency, an interpolation of the requested frequency against the curve created by the above-described method gives the value required by the DAC.